Remedial Classes

2022-2023

Department of Information Technology

GOKARAJU RANGARAJU

INSTITUTE OF ENGINEERING AND TECHNOLOGY

(Autonomous)

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GRIET/PRIN/12A/G/22-23 10-July-2023

GOKARAJU RANGARAJU INSTITUTE OF ENGINEERING & TECHNOLOGY

REMEDIAL CLASSES 2022-23

CIRCULAR

REMEDIAL SCHOOL

This is to inform you all that Remedial Classes will be held for Students to clear their backlogs, from 12th July 2023. List of students and time tables are send to individual departments.

V N Ramadevi

Dean Remedial School

10-07-2023

From

Dean,
Remedial school
GRIET.

Request for faculty to conduct Remedial classes.

Sir/Madam,

This is to inform you that Remedial school of GRIET is conducting Remedial classes for current B.Tech students to clear backlogs in the following subjects from 3:00-4:00 PM from 12-07-2023 to 21-07-2023.

In this context, we request you nominate faculty to teach the following courses:

S. No	Course title	Department	Name of the faculty
1	Digital Logic Design	IT	
2	P&S	H&S	

Thanking you
Yours Sincerely,

V.N. Rama Devi

Tentative Schedule

S.No	Name of the Subject	Faculty Name	18-07-2023	19-07-2023	20-07-2023	21-07-2023
1	DLD	T.N.P. Madhuri				

IT-HoD

S.No	Name of the Subject	Faculty Name	12-07-2023	13-07-2023	214-07-2023	15-07-2023
1	P&S					

IT-H&S

Dean, Remedial Classes

Students List

DLD	
S.No.	**Roll No.**
1	21241A1217
2	21241A1237
3	21241A1247
4	21241A1251
5	21241A1267
6	21241A1270
7	21241A1297
8	21241A12A1
9	21241A12C4
10	21241A12C5
11	21241A12D0
12	21241A12D6
13	21241A12F3
14	21241A12F7
15	21241A12F8
16	21241A12G1
17	21241A12G4
18	21241A12G8
19	21241A12H0
20	21241A12J2
21	21241A12J2
22	21241A12K2

P&S	
S.No.	**Roll No.**
1	21241A1217
2	21241A1237
3	21241A1242
4	21241A1251
5	21241A1267
6	21241A1268
7	21241A12D0
8	21241A12D4
9	21241A12D6
10	21241A12F3
11	21241A12G8
12	21241A12H0
13	21241A12H3
14	22245A1211
15	22245A1216

Gokaraju Rangaraju Institute of Engineering and Technology
Remedial School

Syllabus

Subject : Digital Logic design

Unit I: BINARY SYSTEMS

Digital Systems, Binary Numbers, Number Base Conversions, Octal and Hexadecimal Numbers, Complements, Signed Binary Numbers, Binary Codes, Binary Storage and Registers, Binary Logic.

Boolean Algebra and Logic Gates:

Basic Definitions, Axiomatic definition of Boolean Algebra, Basic theorems and properties of Boolean Algebra, Boolean Functions, Canonical and Standard Forms, Other Logic Operations, Digital Logic Gates, Integrated Circuits.

Unit II: GATE-LEVEL MINIMIZATION

The Map method, Four-variable map, Five-Variable map, Product of Sum's simplifications, Don't care conditions, NAND and NOR implementation, other two level implementations, Exclusive-OR Function.

Unit III: Combinational Logic: Combinational Circuits, Analysis Procedure, Design Procedure, Binary Adder - Subtractor, Decimal Adder, Binary Multiplier, Magnitude Comparator, Decoders, Encoders, Multiplexers.

Unit IV: SYNCHRONOUS SEQUENTIAL LOGIC

Sequential Circuits, Latches, Flip-Flops, Analysis of clocked sequential circuits, State Reduction and Assignment, Design Procedure.

Registers and Counters: Registers, shift registers, Ripple Counters, Synchronous Counters, other counters.

Unit V: MEMORY AND PROGRAMMABLE LOGIC

Introduction, Random Access Memory, Memory Decoding, Error Detection and Correction, Read Only Memory, Programmable Logic Array, Programmable Array Logic, Sequential Programmable Devices.

Hardware Description Language: Hardware Description Language, Definition, Structural Definition of HDL, HDL Models for Combinational circuits, HDL for Models for Sequential circuits.

Remedial Classes 2022-2023

Attendance

S.No	Roll No	18-07-2023	19-07-2023	20-07-2023	21-07-2023
1	18241A1252				
2	18241A12C3				
3	19245A1207				
4	19241A1221				
5	19241A1233				
6	19241A1256				
7	19241A1265				
8	19241A1271				
9	19241A1277				
10	19241A1279				
11	19241A1280				
12	19241A1284				
13	19241A1286				
14	19241A12A6				
15	19241A12A8				
16	19241A12B5				
17	19241A12B9				
18	19241A12C3				
19	19241A12E2				
20	19241A12G3				
Signature of the Faculty					

Computer Organization Classes Room No: 4501

Gokaraju Rangaraju Institute of Engineering and Technology
Remedial School

Topics covered

Subject : Digital Logic Design

I. Important Topics

Unit I: Basic Definitions, Axiomatic definition of Boolean Algebra, Basic theorems and properties of Boolean Algebra, Boolean Functions, Canonical and Standard Forms, Other Logic Operations, Digital Logic Gates, Integrated Circuits.

Unit II: GATE-LEVEL MINIMIZATION

The Map method, Four-variable map, Five-Variable map, Product of Sum's simplifications, Don't care conditions, NAND and NOR implementation, other two level implementations, Exclusive-OR Function.

Unit III: Combinational Logic:

Binary Adder - Subtractor, Decimal Adder, Binary Multiplier, Magnitude Comparator, Decoders, Encoders, Multiplexers.

Unit IV: SYNCHRONOUS SEQUENTIAL LOGIC

Sequential Circuits, Latches, Flip-Flops, Analysis of clocked sequential circuits, State Reduction and Assignment, Design Procedure.

Unit V: MEMORY AND PROGRAMMABLE LOGIC

Introduction, Random Access Memory, Memory Decoding, Error Detection and Correction, Read Only Memory, Programmable Logic Array, Programmable Array Logic, Sequential Programmable Devices.

II. Previous Question Papers Discussed

III. Material shared with the students.

IV. Classes are conducted for Doubts Clarification.

Gokaraju Rangaraju Institute of Engineering and Technology

Remedial School

Student's Feedback on Remedial classes

Branch: IT **Year: II** **Semester:I**

Subject: DLD **Faculty Name: T.N.P.Madhuri**

S.No	Item	Feed back
1.	Material presented	Excellent
2.	Teaching Clarity	Very Good
3.	Coverage of important topics	Good
4.	Doubts clarification	Good

Suggestions: Nil

Gokaraju Rangaraju Institute of Engineering and Technology

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Student's Feedback on Remedial classes

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Results

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14	21241A12F7	
15	21241A12F8	
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Signature of the Faculty		

The following shows the courses for which Remedial classes are held and the Transition rate in such course.

S.No	Subject	No. of students at ended for exam	No. of Students Passed in Exam	Transition Rate
1	DLD	21	*	*

* Results has to release

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15	22245A1216

GOKARAJU RANGARAJU INSTITUTE OF ENGINEERING AND TECHNOLOGY
PROBABILITY AND STATISTICS

Course Code: GR20A2005 **L/T/P/C: 3/0/0/3**
II Year I Semester

Course Objectives

1. Interpret the measures of central tendency and dispersion.
2. Distinguish between explanatory and response variables and analyze data using correlation and regression.
3. Apply various probability distributions.
4. Apply tests of hypothesis.
5. Employ basic analysis of time series data.

Course Outcomes

The expected outcomes of the Course are:

1. Compute and interpret descriptive statistics.
2. Evaluate random processes which occur in engineering applications governed by the Binomial, Poisson, Normal and Exponential distributions.
3. Fit the models using Regression Analysis.
4. Apply Inferential Statistics to make predictions or judgments about the population from which the sample data is drawn.
5. Interpret Time series data.

UNIT I: Random Variables, Basic Statistics, Correlation and Regression

Notion of Randomness, Random Experiment, Random variables – Discrete and Continuous, Probability mass function and density function, constants of r.v.s (Mean, Variance, Monents about mean), Concept of Bivariate distributions and Covariance.
Measures of central tendency and moments.
Correlation : Karl-Pearson's correlation coefficient and Spearman's Rank correlation, Statements of their properties and problems, Simple and Multiple Linear Regression (three variables case only), Statements of properties of Regression coefficients and problems.

UNIT II : Probability Distributions

Discrete Distributions: Binomial and Poisson distributions - definition, real life examples, Statements of their Mean and Variance, related problems, evaluation of statistical parameters.
Continuous Distributions: Normal, Exponential and Gamma distributions - definition, real life examples, Statements of their Mean and Variance and related problems, evaluation of statistical parameters for Normal distribution.

UNIT III : Testing of Hypothesis-1 (Large sample)

Concept of Sampling distribution and Standard error, tests for single proportion, difference of proportions, single mean, difference of means and Chi-square test for independence of attributes. Estimation of confidence interval for population mean and population proportions.

UNIT IV : Testing of Hypothesis-2 (Small Sample)

Tests for single mean, difference of means, Population variance, ratio of variances, ANOVA 1-way and 2-way. Estimation of confidence interval for Population mean.

UNIT V : Time Series analysis

Components of Time series, Additive and Multiplicative Decomposition of Time series components, Measuring trend by method of Moving averages, Straight line and Second degree parabola, Measuring seasonal variation by Ratio to Trend method and Ratio to Moving averages method.

Remedial Classes 2022-2023

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Signature of the Faculty					

Gokaraju Rangaraju Institute of Engineering and Technology

Remedial School

Student's Feedback on Remedial classes

Branch: IT **Year: II** **Semester:I**

Subject: P&S **Faculty Name: G. Srikanth Reddy**

S.No	Item	Feed back
1.	Material presented	Excellent
2.	Teaching Clarity	Very Good
3.	Coverage of important topics	Good
4.	Doubts clarification	Good

Suggestions: Nil

Gokaraju Rangaraju Institute of Engineering and Technology

Remedial School

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Gokaraju Rangaraju Institute of Engineering and Technology

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Branch: IT **Year: II** **Semester:I**

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Gokaraju Rangaraju Institute of Engineering and Technology

Remedial School

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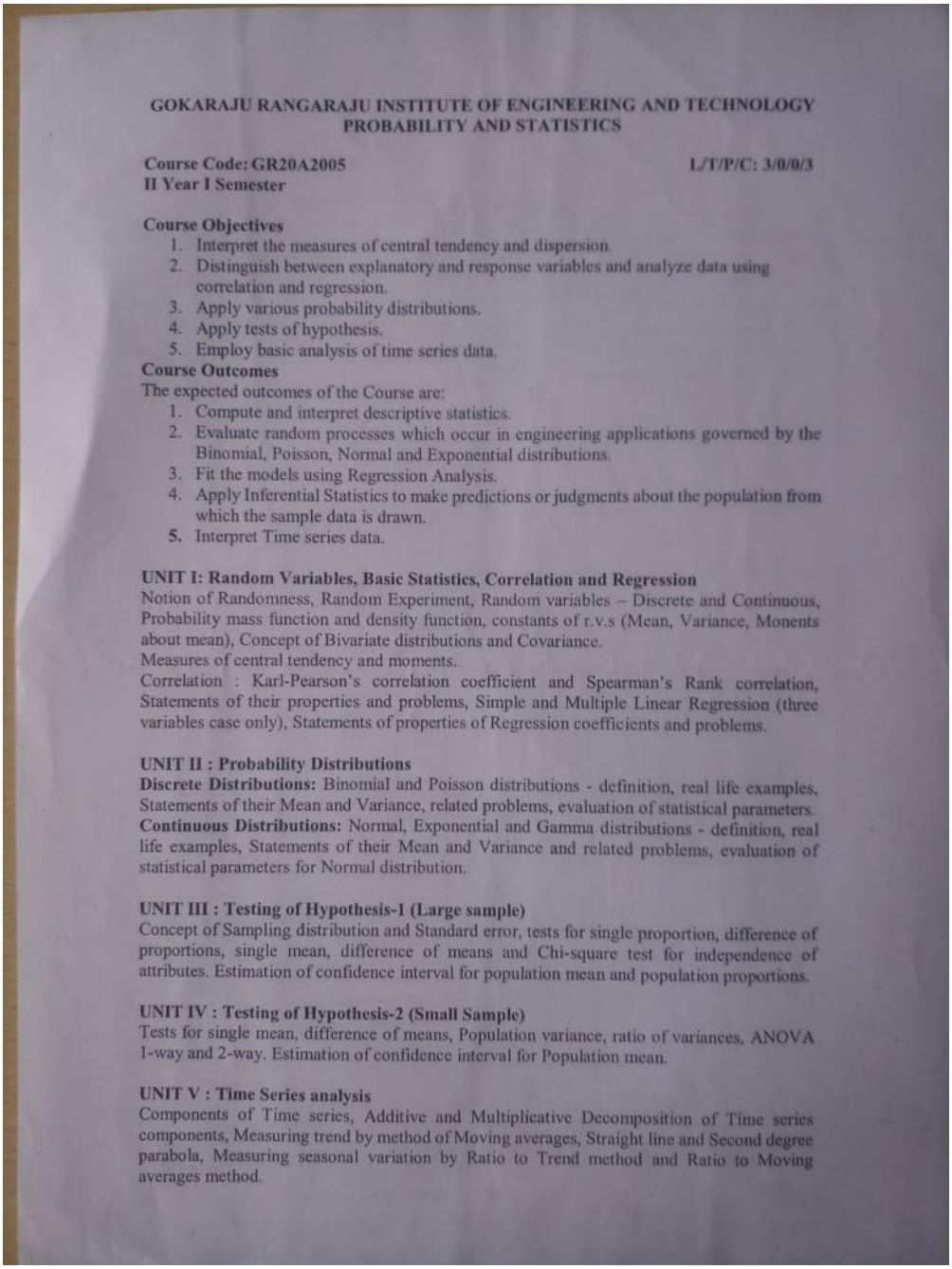
Suggestions: Nil

I.



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